

VISHNUVARDHAN GUNDLAPALLE

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EDUCATION

Indian Institute of Technology, Hyderabad M.Tech in Micro electronics and VLSI, CGPA - 9/10	<i>January 2019 - Present</i>
Rajiv Gandhi University of Knowledge Technologies, IIIT-RK Valley BTech in Electronics and Communication Engineering, CGPA - 9.63/10	<i>July 2014 - May 2018</i>
Rajiv Gandhi University of Knowledge Technologies, IIIT-RK Valley Intermediate in MPC, CGPA - 9.46/10	<i>July 2012 - May 2014</i>
Vivekananda Municipal High School, Madanapalli SSC, CGPA - 9.8/10	<i>July 2011 - April 2012</i>

CAREER OBJECTIVE

To work for an organization which provides me the opportunity to improve my skills and knowledge to grow along with the organization objective.

MAIN PROJECTS

Feature extraction from ECG signal

This project aims in developing Low complex and Robust algorithms for Boundary detection and Feature extraction from ECG signal. And also worked on implementing this process and interfacing with cloud using Firebase on Zed board.

ACADEMIC PROJECTS

Designed and synthesized 16-bit 2D CORDIC hardware
Designed and simulated hardware random number generator

PUBLICATIONS

Naresh Vemishetty, Vishnuvardhan Gundlapalle, A. Acharyya, and Bhudeb Chakravarti, " A Novel 2-lead to 12-lead ECG Reconstruction Methodology for Remote Health Monitoring Applications", Computing in Cardiology (CINC), Singapore, 8-11 September 2019. (Accepted)

TECHNICAL STRENGTHS

Programming	Verilog, Matlab, Basics of C, Java, C#
Software & Tools	Xilinx Vivado, Xilinx ISE, Cadence Virtuoso, Modelsim, Sentaurus
Development Kits	icoboard FPGA iCE40, Digilent ZedBoard ZynqR-7000 ARM/FPGA SoC, Arduino Raspberry Pi

AREAS OF INTEREST

Digital IC Design and Verification, ASIC and FPGA based design, SoC based design.

PERSONAL TRAITS

Highly motivated and eager to learn new things.

Strong motivational and leadership skills.

Ability to work as an individual as well as in group.