

Deepak Kumar

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Technical skills

- **EDA Tools:** Xilinx Vivado, Xilinx ISE, Synopsys DC , IC Compiler ,Cadence Virtuoso , Virtuoso Layout Editor
- **Programming:** Verilog ,Python,Basic C
- **Development Kits:** icoboard FPGA iCE40,Digilent ZedBoard Zynq®-7000 ARM/FPGA SoC
- Working experience with **Raspberry Pi, Arduino**

Work Experience

- Worked as System Validation Engineer for 2 years in Cypress Semiconductor

Areas of Interest

- Digital IC Design and Verification, Physical Design ,ASIC and FPGA based design,SoC based design, CMOS based design.

Education

Academic Qualifications.....

- **Indian Institute of Technology, Hyderabad** **Hyderabad**
M.Tech in Micro electronics and VLSI , CGPA, 9.2/10 2017-present
- **Galgotias College of Engineering and Technology, UPTU** **Greater Noida**
BTech in Electronics and Communication Engineering , Percentage, 76.76 2011–2015
- **DIWAN Public School** **Hapur,UP**
Central Board of Secondary Examination, Percentage, 77.6 2009–2010
- **DIWAN Public School** **Hapur,UP**
Central Board of Secondary Examination, Percentage, 77.2 2007–2008

Academic Projects.....

- Designed a hardware random number generator and implemented on FPGA
- Working on Instruction Set Randomisation (ISR) to avoid code-injection attacks on processors
- Design and simulated the architecture for reconfigurable FFT.
- Design and synthesized 16- bit CORDIC hardware.
- Designed and synthesized 16 - bit Vedic multiplier .
- Design, simulation, verification, layout, DRC, LVS and extraction simulation of half adder circuit..

Relevant Course Work.....

- Digital IC Design*
- Semiconductor Device Physics*
- Mixed Signal Circuit Design*
- Analog IC Design*
- Embedded Systems and Hardware Design
- VLSI Technology and CMOS Fabrication