

# **Student Profile**

Indian Institute of Technology Hyderabad

**Basic Summary** 

Student Name : Chodisetti Murali

Gender : Male

Date Of Birth : 31/05/1994

Summary : Strong interest in circuit design and analysis. Want to associate with the VLSI industry by leveraging the

learned skills for the growth of an organization as well as to enhance my skills by getting exposed to the latest technologies and learning possibilities. Currently working on a project for M.Tech thesis,

developing a battery management system for better battery performance in electric vehicles.

Extracurricular Activities : Interested in playing cricket, table tennis, and badminton

**Current Degrees** 

Degree Batch
M.Tech Micro-Electronics & VLSI(TA) 2018AUG

**Prior Qualifications** 

Degree	Degree Category	Institute	Year	Score Type	Score
10th	Full Time	Pragati Little Public School, Kakinada. Board of	2009	Percentage	90.50
		Secondary Education, Andhra			
10+2	Full Time	Pradesh Narayana Junior College,	2011	Percentage	94.00
		Kakinada. Board of		J	
		Intermediate Education, Andhra Pradesh			
B.Tech Electronics & Communication Eng	Full Time	Koneru Lakshmaiah	2015	CGPI(Scale of	8.67
		University(KLU)		10)	

Skills

Skill	Version	Skill Level	Last Year Used	Years Used	Months Used
Synthesis tools	Synopsis DC and VCS	Beginner	2019	0	2
С	Dev C++	Intermediate	2019	1	6
MATLAB	R2018b	Intermediate	2019	2	0
Digital and analog circuit design	Cadence Virtuso 617	Intermediate	2019	1	0
Verilog	Modelsim 10.4a	Intermediate	2019	1	0
Database	SQL server	Intermediate	2017	1	11

#### **Areas Of Interest**

Interested in electronic circuit design and hardware modelling using HDL

**Project** 

Title : Battery management system for electric vehicles Client :

From : Nov-2018 To : May-2020

Role : developer Role Description : Developing a battery management

system algorithm using kalman filter in matlab. Once verified in matlab, a hardware will be developed which can be integrated with batteries in electric

vehicles.

Team Size : 2 Project Location : IIT Hyderabad

Skills Used : MATLAB, Verilog, FPGA or ASIC design

**Project Details** : Estimating the accurate state of charge, state of health, state of power and state of function of a battery for reducing

the problems of range anxiety, battery ageing, and for improved functional performance in electric vehicles.

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## **Work Experience**

Current Job : No Job Type : Permanent : Full Time

Company Name : Infosys Designation : Systems Engineer

From : Jun-2015 To : May-2017

Job Profile : Web application developer

#### **Work Domain**

**Domain** : IT/ Computers - Software

## Languages

Language	Read	Write	Speak	Proficiency
Hindi	Υ	Υ	N	Proficient
Telugu	Υ	Υ	Υ	Proficient
English	Υ	Υ	Υ	Proficient