

Date: 16 Feb 2018

**CORRIGENDUM – 1**

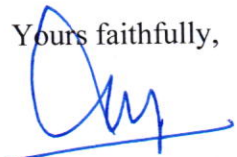
1. It is notified to all concerned parties that with reference to our Tender No. IITH/5(4)/BHABANI/2017/T066 dated 06 February 2018 for supply of "HIGH MEMORY BASED CPU-GPU SERVERS FOR STORE AND ANALYSIS OF HUGE DATA GENERATED FROM COMPUTER SIMULATIONS", the following changes are being made w.r.t. the Tender: -

S.No.	Description	EXISTING	AMENDED AS
(i)	Page No. 11 of 14 of the Tender Document	<b><u>C. Terms and Conditions point # 16</u></b> 16) OEM must have atleast two installation of 50TFlop in India in the last 2 years and and minimum of 2 entries in IISC India supercomputing list, June 2017 or later.	<b><u>C. Terms and Conditions point # 16</u></b>  16) OEM must have atleast two installations of 50TFlop in India in the last 2 years.
(ii)	<b>Revised Due Date &amp; Time for Submission of Tenders is as below: -</b>		
	Last Date & Time for submission of Tender	27 Feb 2018 by 03.00 pm	06 Mar 2018 by 03.00 pm
	Date & Time of Tender Opening	27 Feb 2018 by 03.30 pm	06 Mar 2018 by 03.30 pm

2. If any of you have already submitted your bid prior to issue of this corrigendum you are required to re-submit your bid before the revised Due Date & Time, considering changes in the Tender as per this Corrigendum.

3. All other Terms & Conditions of Tender remain completely unchanged.

Yours faithfully,



(V.VENKAT RAO)  
Joint Registrar  
IIT Hyderabad