



Date: 07 Feb 2019

CORRIGENDUM – 3

1. It is notified to all concerned that with reference to IITH Tender No. **IITH/CE/SURENDRA/2018/T051** dated **14.01.2019** for the supply of “**HPC Cluster**”, the following changes are made w.r.t. the Tender: -

SL. NO.	DESCRIPTION	EXISTING	AMENDED AS
(i)	For Master Node and Compute Node Processor:	2 x Intel SKL 4110 2.2GHz / 10-Core / 13.75MB L3 cache.	2 x Intel SKL 4114 2.2GHz / 10-Core / 13.75MB L3 cache.

2. If any bidder has already submitted the bid prior to issue of this corrigendum, it is required to re-submit the bid before the revised Due Date & Time considering changes in the specifications of the Tender as per corrigendum-2 & corrigendum-3.

3. All other Terms & Conditions of Tender remain completely unchanged.

Yours faithfully,

(Suresh Nair)
Assistant Registrar
IIT Hyderabad