



5G/6G Research Projects at IIT Hyderabad

Applications are invited from the eligible candidates for the below positions from eligible candidates purely on ad hoc basis in the project

Advertisement for Project Associate role in 5G/6G Project IIT Hyderabad

1.	Name of post	Project Associate Engineer
2.	Job Description	<ul style="list-style-type: none"> Software/ Hardware designer without experience but high capability, for example capable of entering public sector companies. The tasks performed are development of design and algorithms.
3.	Essential Qualification	B tech / BE/ M tech ME / MS Master's Bachelor's degree Electrical /Electronic engineering, Computer engineering, /MCA/MSC/Bsc/ BCA , Bachelor of Science or Equivalent practical experience
4.	Skills /Experience Required	<ul style="list-style-type: none"> Domian Knowledge on RTL Design, implementation, and integrations for FPGA Designs. Knowledge with RTL coding using Verilog/System Verilog. Exposure in scripting (Python/TCL). Strong debugging capabilities at RTL simulation and FPGA Emulation
5.	Consolidated Pay	75000
6.	Anticipated start date	01 st June 2025
7.	Tenure of employment	Contract period of 11 months. However, candidate can be reengaged for subsequent terms based on his/her performance and requirement of the Department/Institute.
8.	Mode of selection	Test and interview

9.	Selection Procedure /Mode of application	<p>1 Interested applicant should apply online through the online google form link .</p> <p>https://docs.google.com/forms/d/1-OI3f7z2PEHZVKMfqnv_RlAYyUgXDJkA2OIYQFYwKMA/edit</p> <p>2.The candidate will be shortlisted based on the eligibility criteria, academic record, and relevant experience and selected based on the performance is the Witten interview.</p> <p>3 Date and time of interview: this will be intimated to shortlisted candidates via email.</p> <p>4 IIT Hyderabad reserves the right to alter the number of vacancies or keep it open till the deserving the right to close the recruitment procedure without selecting anybody, if none of the candidates are found suitable .</p> <p>6. Candidates with prior experience in relevant areas will be given preference</p>
----	--	--

10.	Tentative date of interview	Date & Time will be informed through mail to shortlisted candidates
11.	Contact	Candidate may contact: (Email:Srinivas.kvn@5g.iith.ac.in) carrers@5g.iith.ac.in