

Date: 17-01-2020

## Advertisement for the Post of JRF in SERB Sponsored Project

Applications are invited for the following assignment on a purely time-bound research project undertaken in the Department of Electrical Engineering, Indian Institute of Technology Hyderabad.

1.	Name of the post	Junior Research Fellow (JRF)
2.	Number of posts	One (01)
3.	Name of the research project	Design and development of a hybrid DC bus power supply with the high voltage ride through capability and the high fault tolerance
4.	Name of the sponsoring agency	Science & Engineering Research Board (SERB) (A statutory body of the Department of Science & Technology, Government of India)
5.	Duration of the position	One (01) year, extendable to two (02) years (Renewal will be done after one year subjected to satisfactory performance and continued funding availability)
6.	Emolument	INR 31,000 per month consolidated (No HRA)
7.	Essential Qualification	<ul style="list-style-type: none"> <li>▪ M.E/M. Tech in Power Electronics or Electric Drives or Embedded Systems with 60% marks or Equivalent CGPA.</li> </ul>
8.	Desired Qualification	<ul style="list-style-type: none"> <li>▪ Preference shall be given to the candidate with prior knowledge and experience on power electronics converter design (Hardware experience with power devices, signal conditioning circuits, driver circuit, microcontrollers/DSP, Printed circuit boards)</li> <li>▪ Prior Knowledge on Microcontroller/DSP programming, PCB development.</li> </ul>
8.	Last Date for Application	27-01-2020

- **Accommodation & HRA:** This position does not offer accommodation nor HRA
- Eligible applicants should fill the application form and attach relevant documents at <https://forms.gle/AviTy8gQ7E2sGhCn9> or before 27<sup>th</sup> January 2020. You may contact us via email [rupesh@iith.ac.in](mailto:rupesh@iith.ac.in) for any clarifications.
- Candidates will be shortlisted for interview based on their merit and need of the project, and will be informed via email.
- Shortlisted candidates will be invited for interview at IIT Hyderabad. Tentative date of interview is 4th February 2020.
- No TA/DA will be provided for attending the interview.
- The position will remain open if no suitable candidate is found.
- Candidates with desire to pursue research and PhD in future are encouraged.

**Dr. Rupesh Wandhare (PI)**  
**Department of Electrical Engineering**  
**IIT Hyderabad**